

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. IMEC65.1CP1C1	APPLICATION NO. 09/873553
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		RECEIVED	
		FEB 05 2002	
USPTO (SEE SEVERAL SHEETS IF NECESSARY)		FILING DATE June 4, 2001	GROUP 2123 Technology Center 2100

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
RF	5,493,508	2/20/96	Dangelo, et al.	364	489	716/5
RF	5,544,067	8/06/96	Rostoker, et al.	395	500.05	703/14
RF	5,623,418	4/22/96	Rostoker, et al.	395	500.02	716/1
RF	5,726,902	3/10/98	Mahmood, et al.	395	500.07	716/6
RF	5,870,585	2/09/99	Stapleton	395	500.26	703/15
RF	5,923,867	7/99	Hand	703	14	

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
RF	EP0445942A2	2/25/91	Europe	—	—		
RF	EP0772140A1	7/05/97	Europe	—	—		

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
RF	Altmeier, et al., "Generating ECAD Framework Code from Abstract Models", Proc. Of the 32nd ACM/IEEE Conf. On Design Automation, June 1995, pp. 88-93.
RF	Basu, A., et al., "DOORS: An Object-Oriented CAD System for High Level Synthesis", IEE Proceedings - Computers and Digital Techniques, Vol. 144, Issue 5, pp. 331-342, September 1997.
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RF	Burr, et al., "OPERAS in a DSP CAD Environment", Proceedings of the European Design Automation Conference, September 1994, pp. 130-135.
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RF	DeMicheli, G., et al., "The Olympus Synthesis System" IEEE Design & Test of Computers, Vol. 7, Issue 5, pp. 37-53, October 1990.
RF	Forest, John, "Implementation-Independent Description Using an Object-Oriented Approach, in Co-Design-Computer-Aided Software/Hardware Engineering", IEEE Press Marketing, 1995, pps. 263-278.
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RF	Hylander, P., et al., "Object Oriented VLSI Design Automation for Pulse Coded Neural Networks", IEEE International Conference on Neural Networks, Vol. 3, pp. 1825-1829, July 1994.
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EXAMINER	RUSSELL FREED	DATE CONSIDERED	9.23.04
*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.			

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	APPLICANT Schaumont, et al.	GROUP 2123
FILING DATE June 4, 2001		

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
RF	Lavenier, D., et al., "From Behavioral to RTL Models" An Approach", Proceedings of the Fifth International Workshop on Rapid System Prototyping, pps. 153-161, June 1994.
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RF	Sarkar, S., et al., "Synchronization of Communicating Modules and Processes in High Level Synthesis", Proc. Of the 8 th International Conference on VLSI Design, a995, pp. 87-92, January 1995.
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U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
IMEC65.1CP1C1APPLICATION NO.
09/873,553INFORMATION DISCLOSURE STATEMENT
BY APPLICANT

(USE SEVERAL SHEETS IF NECESSARY)

APPLICANT
Schaumont, et al.FILING DATE
June 4, 2001GROUP
2123

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
RF	1	5,933,356	08/03/99	Rostoker et al.	703	15	
RF	2	6,064,819	05/16/00	Franssen et al.	717	156	
RF	3	6,233,540	05/15/01	Schaumont et al.	703	14	
RF	4	6,606,588	08/12/03	Schaumont et al.	703	15	
RF	5	6,324,678	11/27/01	Dangelo, et al.	716	18	

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
RF	6	Verkest, et al., 1996, pp. 357-386.
RF	7	Woo, et al., 1994, pp. 42-47.
RF	8	Yeh, et al., 1994, pp. 130-135.

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